

REMARKS

Claims 1 – 7, 9 – 13, and 15 – 39 are presently pending. In the above-identified Office Action, the Examiner rejected Claims 1 – 5, 9 – 11 and 15 – 22 under 35 U.S.C. § 103(a) as being unpatentable over Hynecek ('070) in view of Tashiro ('062). Claims 6, 7, 12, 13 and 29 – 33 were rejected under 35 U.S.C. § 103(a) as being unpatentable over Hynecek ('070) in view of Tashiro ('062) and further in view of Hynecek ('106). Claims 23 – 25 were rejected under 35 U.S.C. § 103(a) as being unpatentable over Hynecek ('070) in view of Tashiro ('062) further in view of Yu ('843). Claims 26 – 28 were rejected under 35 U.S.C. § 103(a) as being unpatentable over Hynecek ('070) in view of Tashiro ('062) further in view of Shimomura *et al.* ('164). Claims 34 – 36 were rejected under 35 U.S.C. 103(a) as being unpatentable over Hynecek ('070) in view of Tashiro ('062) and Hynecek ('106) further in view of Yu ('843). Claims 37 – 39 were rejected under 35 U.S.C. § 103(a) as being unpatentable over Hynecek ('070) in view of Tashiro ('062) and Hynecek ('106) further in view of Shimomura *et al.* ('164).

By this Paper, Applicants have amended Claims 1 and 9 to include the limitations of Claims 5 and 11 respectively. For the reasons set forth more fully below, Applicants respectfully submit that the invention properly defines an invention patentable over the prior art. Reconsideration, allowance and passage to issue are therefore respectfully requested.

The invention addresses the need in the art for a system or method for imaging in the visible spectrum with high detector density at low cost. In accordance with the invention, the need is addressed with a detector having a body configured to float and being biased whereby the output thereof is responsive to electromagnetic energy.

The invention is set forth in Claims of varying scope of which Claim 1 is illustrative. Claim 1 recites:

1. A detector comprising:
a transistor sensitive to electromagnetic energy, said transistor having a body, a gate terminal, a source terminal and a drain terminal and **the body of said transistor being configured to float** and
means for biasing said transistor whereby an output thereof is responsive to said electromagnetic energy.

None of the references, taken alone or in combination, teach, disclose or suggest the invention as presently claimed. That is, none of the references teach, disclose or suggest a detector biased to be responsive to electromagnetic energy with a body configured to float.

In the above-identified Office Action, rejected Claims 5 and 11 under 35 U.S.C. § 103(a) as being unpatentable over Hynecek ('070) in view of Tashiro ('062). In this connection, the Examiner suggested that in Figs. 2 and 3 and at col. 6, lines 57 – 66, Hynecek ('070) discloses that the body of the transistor is configured to float. However, this assertion is not supported by the teachings of the reference. The passage noted by the Examiner appears to merely disclose the doping of the gate region 70. No mention is made of a 'floating' body configuration or a transistor configured to 'float'. As is well known in the art, a floating body configuration is one in which a channel terminal of a device is isolated from a substrate. Hynecek clearly does not mention either a floating body configuration or an isolation of a channel of a transistor from a substrate thereof.

Hence, the invention as claimed should be allowable. Reconsideration, allowance and passage to issue are therefore respectfully requested.

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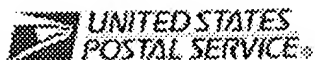


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